

DAFTAR PUSTAKA

- [1] Akagi, H., Kanazawa, Y., Nabae, A. "Instantaneous reactive power compensator comprising switching devices without energy storage components", *IEEE Trans. Ind. Appl.*, 1984, I, (3), pp.625-630
- [2] Hamid, M.I., Jusoh, A., Anwari, Makbul., " Photovoltaic Plant With Reduced Output Current Harmonics Using Generation-side Active Power", *IET Renew. Power Gener.*, 2014, pp. 1-10
- [3] Munadi, R., Hayati, R., Irhamsyah, M., Arnia, F. "Performansi Filter Digital FIR Dan IIR Pada Pengolahan Sinyal Radar", Jurusan Teknik Elektro Fakultas Teknik Universitas Syiah Kuala, 2004,1, (3), pp. 23-30
- [4] Irmawan, S.Si,MT., "Perancangan Dan Simulasi *Low pass* Finite Impulse Response Dengan Metode Windowing", Jurusan Teknik Elektro Fakultas Teknik Universitas Sriwijaya
- [5] Chakraborty, S. "Design and Realization of Digital FIR Filter using Dolph-Chebyshev Window", *IJCSTEC, Department of Electronics and Communication Engineering Calcutta Institute of Technology.*, ISSN: 2229-3345., 2013,07, (4), pp. 987-996
- [6] Lynch, P. "The Dolph-Chebyshev Window: A Simple Optimal Filter", Met Eireann, Glasnevin Hill, Dublin, Ireland, American Meteorological Society, 1997,(125), pp. 655-660
- [7] Hayati, R*, Kurnia, R**. "Simulasi Unjuk Kerja Discrete Wavelet Transform (DWT) Dan Discrete Cosine Transform (DCT) Untuk Pengolahan Sinyal Radar Di Daerah Yang Ber-Noise Tinggi". *Jurusan Teknik Elektro, Politeknik Negeri Lhokseumawe., **Fakultas Teknik, Jurusan Teknik Elektro, UNAND. ISSN:2302-2949, 2014,1, (3). pp. 32-43

[8] Sugiarto, H., “Mereduksi Harmonisa Arus Dan Rugi Daya Akibat Beban Non Linier Dengan Memanage Penggunaan Beban Listrik Rumah Tangga”., Jurusan Teknik Elektro Politeknik Negeri Pontianak., *Jurnal ELKHA*, 2015,1, (7), pp. 34-40

[9] Mooniarsih, C.T., “Desain dan Simulasi Filter FIR Menggunakan Metode Windowing”. Laboratorium Telekomunikasi, Jurusan Teknik Elektro, Fakultas Teknik, Universitas Tanjungpura., *Jurnal ELKHA*, 2010,1, (2), pp. 41-46

[10] [http://learn.mikroe.com/ebooks/digital filter design](http://learn.mikroe.com/ebooks/digital%20filter%20design), diunduh pada tanggal 20 Januari 2016

[11] Duane Hanselman and Bruce Littlefield. 2000. Matlab Bahasa Komputasi Teknis: Komputasi, Visualisasi, dan Pemograman (terjemahan oleh Andi). Yogyakarta Giancoli, Douglas c. 2001. Fisika 2 (edisi kelima). Jakarta: Erlangga.

[12] Valkenburg, M.E. Van. 1982. Analog Filter Design. Japan: CBS College Publishing.

[13] Chen, Wai-Kai. 1986. Passive And Active Filters.

[14] Timbus, A., Lieserre, M., Teodorescu, R.,Blaabjerg, F.: ‘Synchronization Methods for Three Phase Distributed Power Generation Systems. An Overview and Evaluation’. *Proc. IEEE 36th Conf. on Power Electronics Specialists, Recife, Brazil, 2005, pp.1250-1255*

[15] Carugati, I., Maestri, S., Donato, P.G., Carrica, D., Benedetti, M.: ‘ Variable Sampling Period Filter PLL for Distorted Three-Phase System’, *IEEE Trans. Power Electron.*, 2012, 27, (1), pp. 321-330

[16] Guo, X. –Q., Wu, W.Y.: ‘Simple Synchronization Technique for Three-Phase Grid-Connected Distributed generation System’, *IET Renew. Power Gener.*, 2013, 7, (1), pp. 55-62



[17] Garcia-reteguir, R., Gonza, S.A., Funes, M.A., Maestrit, S.: ‘Implementation of Novel Synchronization Method using Sliding Geortzel DFT’. *Proc. IEEE Int. Symp. On Intelligent Signal Processing, Alcalá de Henares, October 2007, pp. 1-5*

[18] Rodriguez, P.C., Teodorescu, R.E., Candela, I.C., Timbus, A.V.E., Liserre, M.I., Blaabjerg, F.E.: ‘New Positive-sequence Voltage Detector for Grid Synchronization of Power Converters Under Faulty Grid Conditions’. *Proc. IEEE 37th Power Electronics Specialists Conf., Jun 2006, pp. 1-7*

